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From: Robert D. Marshall, Jr.  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of TI-30481  
Gary L. Swoboda, et al. Art Unit: 2128  
Serial No.: 09/943,599 Examiner: Akash Saxena  
Filed: August 30, 2001 Conf. No.: 2479  
For: Correlating On-Chip Data Processor Trace Information for Export

**CERTIFICATION OF FACSIMILE TRANSMISSION**

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Robin E. Barnum

June 21, 2005  
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**FACSIMILE COVER SHEET**

<input checked="" type="checkbox"/> <b>FACSIMILE COVER SHEET</b>	<input checked="" type="checkbox"/> <b>AMENDMENT</b> <u>111</u> (25 Pages)
<input type="checkbox"/> <b>NEW APPLICATION</b>	<input type="checkbox"/> <b>EOT</b> (# Page)
<input type="checkbox"/> <b>DECLARATION (# Pages)</b>	<input type="checkbox"/> <b>NOTICE OF APPEAL (# Pages)</b>
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<input type="checkbox"/> <b>CONTINUATION APP'N (# Pages)</b>	
<input type="checkbox"/> <b>DIVISIONAL APP'N</b>	
<b>NAME OF INVENTOR(S):</b> Gary L. Swoboda, et al.	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> Serial No.: 09/943,599 Filing Date: August 30, 2001	
<b>TITLE OF INVENTION:</b> Correlating On-Chip Data Processor Trace Information for Export	
<b>TI FILE NO.:</b> <b>TI-30481</b>	<b>DEPOSIT ACCT. NO.:</b> 20-0668
<b>FAXED:</b> 6/21/05 <b>DUE:</b> 6/22/05	<b>ATTY/SEC'Y:</b> RDM/reb

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Applicant: Swoboda et al                      Art Unit: 2128  
Serial No.: 09/943,599                      Examiner: Akash Saxena  
Filed: August 30, 2001                      Docket: TI-30481  
For: CORRELATING ON-CHIP DATA PROCESSOR TRACE INFORMATION FOR  
EXPORT

AMENDMENT UNDER 37 C.F.R. 1.111

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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UNDER 37 C.F.R. 81.6(b)

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Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated March 22, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: